

# WEST Search History





DATE: Tuesday, January 18, 2005

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
	<i>DB=PGPB,USPT,JPAB; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L23	L22 not l21	12
<input type="checkbox"/>	L22	L19 and pressure	25
<input type="checkbox"/>	L21	L19 and gate	22
<input type="checkbox"/>	L20	L19 and logic	13
<input type="checkbox"/>	L19	L16 and component	34
<input type="checkbox"/>	L18	L16 and symbol\$	4
<input type="checkbox"/>	L17	L16 and symbol	4
<input type="checkbox"/>	L16	L15 and functional	34
<input type="checkbox"/>	L15	l13 and (hdl or vhdl or verilog or software)	39
<input type="checkbox"/>	L14	l13 and (hdl or vhdl or verilog)	4
<input type="checkbox"/>	L13	L12 and (layout or placement)	57
<input type="checkbox"/>	L12	L6 and schematic	114
<input type="checkbox"/>	L11	L9 and simulati\$	29
<input type="checkbox"/>	L10	L9 and (analog\$ or digital or mixed or hybrid)	21
<input type="checkbox"/>	L9	verilog same api	29
<input type="checkbox"/>	L8	api adj verilog	0
<input type="checkbox"/>	L7	L6 and symbol\$	18
<input type="checkbox"/>	L6	L1 and connecti\$3 and port	168
<input type="checkbox"/>	L5	L1 and connectivity and port	4
<input type="checkbox"/>	L4	L3 not l2	5
<input type="checkbox"/>	L3	L1 and simulat\$ and port	43
<input type="checkbox"/>	L2	L1 and simulat\$ and port and (placement or placing)	38
<input type="checkbox"/>	L1	(microfluid\$ or nanofluid\$) same channel same circuit	298

END OF SEARCH HISTORY

# WEST Search History

Hide Items

Restore

Clear

Cancel

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Hide?	Set Name	Query	Hit Count
		<i>DB=PGPB,USPT,JPAB; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L11	L10 and circuit and logic	25
<input type="checkbox"/>	L10	L2 and (fluid\$ adj channel) and simulat\$4	161
<input type="checkbox"/>	L9	L8 and simulat\$4	26
<input type="checkbox"/>	L8	L4 and schematic and (fluid same channel)	50
<input type="checkbox"/>	L7	L6 and (fluid same channel)	45
<input type="checkbox"/>	L6	L5 and (placement or placing)	62
<input type="checkbox"/>	L5	L4 and channel	77
<input type="checkbox"/>	L4	L3 and gate and switch\$	84
<input type="checkbox"/>	L3	L2 and layer and logic	386
<input type="checkbox"/>	L2	microfluid\$ or nanofluid\$	8573
<input type="checkbox"/>	L1	phan-thai.xp.	27

END OF SEARCH HISTORY